

KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE: WARANGAL - 15
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
M.Tech Digital Communication (II-Semester) Class work Time Table (2015-16)

Class Teacher: Smt. Ch.Navitha

Room. No.BIV-001B

w.e.f. 25.01.2016

CDay / Time	9.40 - 10.30	10.30 -11.20	11.20 -12.10	12.10 - 1.00		2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Monday	RSP	CT	SC	-	L	←-----DC Lab-----→ BN / AZS		
Tuesday	-	ASP	CT	CSM	U	DIP	RSP	SC
Wednesday	-	DIP	CT	ASP	N	SC	CSM	RSP
Thursday	-	CSM	ASP	DIP	C	←-----CSM Lab-----→ VVR/RS		
Friday	ASP	CSM	SC	RSP	H	CT	DIP	
Saturday	←-----T&P/CCA/JKC-----→							

ASP	: Adaptive Signal Processing	:	Smt. CH. Navitha (CHN)
DIP	: Digital Image Processing	:	Sri. B Narasimha (BN)
RSP	: Radar Signal Processing	:	Sri P.Chiranjeevi (PC)
CT	: Coding Theory	:	Sri. R. Srikanth(RS)
CSM	: Communication System Modeling	:	Sri V. Venkateshwar Reddy (VVR)
SC	: Satellite Communications	:	Sri. M.Chandrashekar (MC)
DC Lab	: Digital Communications Lab	:	BN/AZS
CSM Lab	: Communication System Modeling Lab	:	VVR/RS
ECEA	: ECE Association Activity	:	B.Komuraiah/Syed Zaheeruddin /
		:	P. Chiranjeevi/E.Susmitha
T&P / CCA / JKC	: Training & Placement /Cultural & Co-curricular Activity/ Jawaharlal Knowledge Center Activity	:	Sri. A.Srinvas /Sri K.Ramudu


I/c Time Table


Coordinator
(Time Tables)


Head, Dept of ECE


Dean, Academic Affairs


PRINCIPAL