

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL-15
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
Class work Time -Table for the year 2015-2016
M.Tech. (Power Electronics) II-Semester

Class Teacher: Sri G. RAJENDRA NAIK

Class Room: LH-5

w.e.f. 25.01.2016

DAY\TIME	9.40-10.30	10.30-11.20	11.20-12.10	12.10-1.00		2.00-2.50	2.50-3.40	3.40-4.30
MONDAY	PQ	AIAEE	PECAC	DSP	L	APE	FACTS	FACTS
TUESDAY	PECAC	APE	APE	PQ	U	DSP	AIAEE	-
WEDNESDAY	DSP	PECAC	PECAC	AIAEE	N	← ED LAB NR →		
THURSDAY	AIAEE	PQ	PQ	FACTS	C	DSP	APE	-
FRIDAY	FACTS	← DSP LAB HRK →			H			
SATURDAY								

PECAC: Power Electronic Control of AC Drives

DSP: Digital Signal Processors

APE: Advanced Power Electronics

AIAEE: Artificial Intelligence Applications in Electrical Engineering

PQ: Power Quality

FACTS: Flexible AC Transmission Systems

ED LAB: Electric Drives Laboratory

DSP LAB: Digital Signal Processors Laboratory

Sri N. RAKESH

Sri H. RANJITH KUMAR

Sri M. SRINIVAS

Sri G. RAJENDRA NAIK

Sri G. SUNIL KUMAR

Sri P. RAVI TEJA

Sri N. RAKESH

Sri H. RANJITH KUMAR


I/C Time Table


Coordinator Time Tables


Head of the Department


Dean, Academic Affairs


Principal