

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL - 15
(An Autonomous Institute under Kakatiya University, Warangal)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
 IV Semester Class work Time Table (2016-2017)

Class Teacher: Prof.M.Sreelatha

Class Room . B I 202

w.e.f. 09.12.2016

Day	9.40 - 10.30	10.30 -11.20	11.20 -12.10	12.10 - 1.00	L U N C H	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Mon	EM-IV	DD using VHDL	CAO	SS		←----DD LAB (B1)/LIC LAB (B2)-----→		
Tue	LIC	S&T	SS	EM-IV		DD using VHDL (T) B IV ---BJ	←-----SIS-----→ BIV-130	
Wed	S&T	SS	CAO	LIC (T) BKS		EM-IV	DD using VHDL	RC/L
Thu	←----DD LAB (B2)/LIC LAB (B1)-----→			EIEA		LIC	S&T (T) KSC	CAO (T) BKS
Fri	CAO	LIC	DD using VHDL	S&T		SS (T) BS	EM-IV (T)	RC/L
Sat	CCA/JKCA/T&P/ES							

EM-IV	:	Engineering Mathematics - IV	:	Sri D.Srikanth
DD using VHDL	:	DD using VHDL	:	Dr.K.Srinivas
CAO	:	Computer Architecture & Organization	:	Sri B.Jeevan
SS	:	Signals & Systems	:	Prof.M.Sreelatha
LIC	:	Linear Integrated Circuits	:	Dr.K.Venu Madhav
S&T	:	Sensors & Transducers	:	Sri B.Shashikanth
DD LAB	:	Digital Design Laboratory	:	Dr.K.Srinivas, Md.Abdul Muqueem, Sri B.Jeevan ---(B1) Dr.K.Srinivas, Md.Abdul Muqueem, Smt K.Shailaja ---(B2)
LIC LAB	:	Linear Integrated Circuits Laboratory	:	Sri B.Shashikanth, Sri G.Raju, Sri B.Krishna Sundeep----(B1) Sri B.Shashikanth, Sri G.Raju, Smt B.Smitha----(B2)
SIS	:	Soft & Interpersonal Skills	:	Sri P.Anvesh/Ms.G.Neelaveni
RC/LC	:	Remedial & Library Class	:	
CCA/JKCA/T&P/ES: Cultural and Co Curricular Activitiy				

Smitha
I/c Time Table
(Smt B.Smitha)

Andal
Coordinator
(Time Tables)
Dr.M.Andal

Sivani
HoD, Dept. of E&I
(Dr.K.Sivani)

C. Venkatesh
Dean,
7/12/16
Academic Affairs
(Prof.C.Venkatesh)

Rao
PRINCIPAL
(Prof.P.Venkateswar Rao)