

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL - 15
(An Autonomous Institute under Kakatiya University, Warangal)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
VI Semester Class work Time Table (2016-2017)


Class Teacher: Smt R.Nirmala Devi


Class Room BIV 013


w.e.f. 09.12.2016


Day	9.40 - 10.30	10.30 -11.20	11.20 -12.10	12.10 - 1.00	L U N C H	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Mon	PM (OE-I)	DIP	A&DC (T) RS	MC&ES		VLSID	PC	RC/L
Tue	PC	←--MC&ES Lab (B1)/Sim.Lab(B2)-----→				A&DC	MC&ES(T) MQ	RC/L
Wed	DIP	MC&ES	VLSID	PM (OE-I)		←--MC&ES Lab (B2)/Sim.Lab(B1)-----→		
Thu	MC&ES	PC	VLSID	EIEA		A&DC	DIP	PM (OE-I)
Fri	A&DC	PM (OE-I)	VLSID (T) BJ	PC (T) BVM		DIP	←-Mini Project--→	
Sat	CCA/JKCA/T&P/ES							

A&DC	:	Analog & Digital Communications	:	Sri A.Pavan
MC&ES	:	Microcontrollers & Embedded Systems	:	Dr.K.Venumadhav
VLSID	:	VLSI Design	:	Dr.K.Sivani
PC	:	Process Control	:	Sri M.Raghu Ram
OE-I (PM)	:	Project Management	:	Md.Abdul Muqueem
PE-I (DIP)	:	Digital Image Processing	:	Smt. R.Nirmala Devi
MC&ES LAB	:	Microcontrollers & Embedded Systems Laboratory	:	Dr.K.Venu Madhav, Md.Abdul Muqueem, Sri O.Anjaneyulu----(B1) Dr.K.Venu Madhav, Md.Abdul Muqueem, Dr.K.Srinivas----(B2)
SIM.LAB	:	Simulation Laboratory	:	Dr.K.Sivani, Smt R.Nirmala Devi, Sri B.Jeevan ----(B1) Prof.M.Sreelatha, Smt R.Nirmala Devi, Sri B.Jeevan ----(B2)
MINI PROJECT	:	Mini Project	:	Sri B.Shashikanth, Md.Abdul Muqueem
RC/LC	:	Remedial Class/Library Class	:	
CCA/JKCA/T&P/ES: Cultural and Co Curricular Activitiy				


I/c Time Table
(Smt B.Smitha)


Coordinator
(Time Tables)
Dr.M.Andal


HoD, Dept. of E&I
(Dr.K.Sivani)


Dean,
Academic Affairs
(Prof.C.Venkatesh)


PRINCIPAL
(Prof.P.Venkateswar Rao)