



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15  
(An Autonomous Institute under Kakatiya University, Warangal)

## Office of the Controller of Examinations

No. B.Tech/VI-Sem/Labs/2017

Date: 06-11-2017

Schedule of VI-Semester B.Tech. (Supplementary) Practical Exams, December, 2017

BRANCH: **CIVIL ENGINEERING**

Subject / Time	09-00 AM to
Date	12-00 NOON

HIGHWAY ENGINEERING LAB:

Nil


GEOTECHNICAL ENGINEERING LAB:

08-12-2017 A

Batch No.    H.T.Nos.

A    -    B14CE029, B14CE069, B14CE072, B14CE088, B14CE089, B14CE117

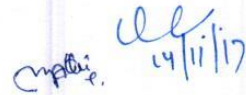
Any omission or clash should be informed to the Controller of Examinations immediately, in writing.

  
Controller of Examinations

To: All Notice Boards

Copy to:

1. The Director for Information
2. All Deans
3. All Heads of Depts.
4. Academic Section
5. Web Team

  
14/11/17



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15  
(An Autonomous Institute under Kakatiya University, Warangal)

## Office of the Controller of Examinations

No. B.Tech/VI-Sem/Labs/2017

Date: 06-11-2017

Schedule of VI-Semester B.Tech. (Supplementary) Practical Exams, December, 2017

### BRANCH: ELECTRICAL & ELECTRONICS ENGINEERING

Subject / Time 09-00 AM to  
Date 12-00 NOON

#### POWER ELECTRONICS & SIMULATION LAB:


08-12-2017 A

#### ELECTRICAL MACHINES-II LAB:

09-12-2017 B

<u>Batch No.</u>	<u>H.T.Nos.</u>
A -	B14EE051, B14EE067
B -	B14EE067

Any omission or clash should be informed to the Controller of Examinations immediately, in writing.

  
Controller of Examinations

To  
All Notice Boards

Copy to:

1. The Director for Information
2. All Deans
3. All Heads of Depts.
4. Academic Section
5. Web Team

*Handwritten notes:*  
LCE  
14/11/17  
Cmp/11/17



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15  
(An Autonomous Institute under Kakatiya University, Warangal)

## Office of the Controller of Examinations

No. B.Tech/VI-Sem/Labs/2017

Date: 06-11-2017

Schedule of VI-Semester B.Tech. (Supplementary) Practical Exams, December, 2017

### BRANCH: ELECTRONICS & INSTRUMENTATION ENGINEERING

Subject / Time	09-00 AM to
Date	12-00 NOON

#### SIMULATION LAB:

08-12-2017                      A

#### MICRO CONTROLLERS & EMBEDDED SYSTEMS LAB:

09-12-2017                      A

Batch No.      H.T.Nos.

A      -      B15EI064L

Any omission or clash should be informed to the Controller of Examinations immediately, in writing.

Controller of Examinations

To : All Notice Boards

Copy to:

1. The Director for Information
2. All Deans
3. All Heads of Depts.
4. Academic Section
5. Web Team

lll  
14/11/17  
Copy to



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15  
(An Autonomous Institute under Kakatiya University, Warangal)

## Office of the Controller of Examinations

No. B.Tech/VI-Sem/Labs/2017

Date: 06-11-2017

Schedule of VI-Semester B.Tech. (Supplementary) Practical Exams, December, 2017

### BRANCH: MECHANICAL ENGINEERING

Subject / Time 09-00 AM to  
Date 12-00 NOON

#### HEAT TRANSFER LAB:

08-012-2017 A

#### PRINCIPLES OF METAL CUTTING LAB:

Nil

#### COMPUTER AIDED ANALYSIS LAB:

09-12-2017 B

#### Batch No.    H.T.Nos.

A    -    B14ME106, B15ME141L, B15ME142L  
B    -    B14ME065

Any omission or clash should be informed to the Controller of Examinations immediately, in writing.

*RG - 2017*  
Controller of Examinations

To: All Notice Boards

Copy to:

1. The Director for Information
2. All Deans
3. All Heads of Depts.
4. Academic Section
5. Web Team

*CC*  
*14/11/17*  
*Chaitanya*