KAKATIYA INSTITUTE OF TEHCNOLOGY & SCIENCE : : WARANGAL-15.
EXAMINATION SECTION

No. M.Tech.-II Sem./I-Mid exam/2013   Date: 11-07-2013

TIME-TABLE

The M.Tech II Semester I Mid examinations will be commencing from **05.08.2013 (Monday)** to **12-08-2013 (Monday).**

Time of examination: 02.00 PM to 04.00 PM

Date & Day	Structural & Const. Engg.	Design Engg.	Digital Communication	Software Engg.	VLSI & Embedded Systems
05-08-2013 (Monday)	Theory of Elasticity and Plasticity	Finite Element Analysis	Coding Theory	Advanced DBMS	Mixed Signal Design
06-08-2013 (Tuesday)	Design of Bridges	Advanced Mechanisms Design & Analysis	Communication System Modeling	Information Systems & Auditing	VLSI Physical Design
07-08-2013 (Wednesday)	Seismic analysis of structures	Advanced Materials Science and Engineering	Multimedia Communications & System Design	Real Time Systems	Embedded System Modeling, Synthesis and Verification
08-08-2013 (Thursday)	Construction planning and Management	Automation & Robotics	Digital Image Processing	Software Architecture	Low power VLSI Design
10-08-2013 (Saturday)	Personal Management	MEMS & Nano Technology	Elective-II DSP Processors	Software Quality Assurance & Testing	Design for Testability
12-08-2013 (Monday)	Elective-II Finance Management	Elective-II Fault diagnosis of machines	Elective-III Adaptive Signal Processing	Elective-II Data mining & Data Warehousing	Elective-II Cellular & Mobile Communications

PRINCIPAL

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