



**MINUTES OF THE MEETING OF BOARD OF STUDIES (BoS) in  
ELECTRICAL AND ELECTRONICS ENGINEERING  
held at 10.30 AM on 03.08.2024 (Saturday)  
Venue: Digital Simulation laboratory**

**Agenda:**

- 1. Approval of the scheme of instruction of B.Tech. EEE program under URR24**
- 2. Approval of the syllabus of B.Tech. EEE program - I & II semesters under URR24**
- 3. Approval of rules and regulations under URR24**
- 4. Any other item with the permission of the Chair**

**Members Present:**

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|-----|---|-----|--|
| 1.  | Dr. G. Rajendar<br><i>Associate Professor &amp; HoD of EEE, KITSW</i>                               | --- | Chairperson, BoS   |
| 2.  | Dr. M. Shailaja Kumari<br><i>Professor of EEE, NIT, Warangal</i>                                    | --- | External Member<br><i>(Subject expert from Institute of National Importance)</i> |
| 3.  | Dr. S. Srinivas<br><i>Professor of EEE, IIT, Madras</i>   | --- | External Member<br><i>(Subject expert from Institute of National Importance)</i> |
| 4.  | Prof. Francesco Grimaccia<br><i>Dept. of Energy Politecnico di Milano, Italy</i>                    | --- | External Member<br><i>(Subject expert from outside the country)</i>              |
| 5.  | Dr. T. Jaganmohan Raj<br><i>Associate Professor of EEE, UCE, Kothagudem</i>                         | --- | External Member<br><i>(University Nominee)</i>                                   |
| 6.  | Sri T. Srimannarayana Murthy<br><i>Chief Engineer, TSGENCO</i>                                      | --- | External Member<br><i>(Industry Representative)</i>                              |
| 7.  | Sri E. Ram Mohan Rao<br><i>Associate Consultant, TCS, Hyderabad</i>                                 | --- | External Member<br><i>(Industry Representative)</i>                              |
| 8.  | Dr. Vashist Bist<br><i>Systems Engineer, Allegro Micro Systems, Pune</i>                            | --- | External Member<br><i>(Industry Representative)</i>                              |
| 9.  | Dr. Sandeep Madishetti<br><i>Principal R&amp;D Engineer, Lite-On Singapore Pvt. Ltd., Singapore</i> |     | External Member<br><i>(Industry Representative)</i>                              |
| 10. | Sri Ch. Ramesh<br><i>Manager, Designs, CG Railway Business Division, Bhopal, Madhya Pradesh</i>     | --- | External Member<br><i>(UG Alumni)</i>  |

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|----|---|-----|--------------------------------|
| 11 | Sri G. Balajose<br><i>Senior Manager, Software Engineering<br/>Sales force, Hyderabad</i> | --- | External Member<br>(PG Alumni) |
| 12 | All faculty of the department   | --- | Members                        |

1. BoS meeting of Electrical and Electronics Engineering Department was conducted on 03.08.2024, from 10:30 AM to 2:00 PM at B-III 203 (Digital Simulation Laboratory), KITS, Warangal.
2. One (01) external member and all internal BoS members were present physically. Nine (09) external BoS members attended virtually (online mode) through the GOOGLE MEET platform
3. All the BoS members were present and offered their valuable suggestions.

<b>Details of the meeting</b>
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The meeting commenced at 10:30AM and was presided over by the Chairman, BoS. At the outset, the Chairman, BoS welcomed the members to the meeting to discuss the pre-notified items on the agenda and sought approval.

- At the outset, the Chairman presented his views that the engineering programmes are required to impart required Knowledge (K), Skills (S) and Qualities (Q) (values and attitude) and facilitate students to acquire the characteristics of a good engineer. The KSQ imparted to the students will make them industry-ready and also capable of entering Institutes of National Importance for higher education and research.
- The chairman informed the BoS members that industry experts and alumni have been contacted for inputs in preparing URR24 industry ready curriculum. The expected competencies (both Technical and generic competencies) to be demonstrated **by the graduates of B.Tech. EEE programme** along with the **target industries/research organizations** have been identified. The courses (theory & Labs) for the B.Tech. EEE programme have been identified by mapping the desired competencies with courses and **blueprint of the scheme** has been prepared.
- Learning tracks for Placements (core & IT/ITES), higher education and research are identified for the benefit of students
- The members of BoS have been informed about the following new components that are newly introduced in the curriculum under URR24 regulations.

Component introduced	Semester	Total credits	Justification
Multidisciplinary Open Electives Courses (MOPEC)	V, VII & VIII	9	To give exposure on interdisciplinary and cross disciplinary domains.
Practicum	I, II, III & IV	4	For experiential learning to impart problem-solving, critical thinking, and communication skills
Startups & Entrepreneurship (STE) basket	V	3	To inspire and prepare the graduates with startup and entrepreneurial mindset
Social Empowerment Activities (SEA) and Self Accomplishment Activities (SAA)	I, II, III & IV	4	To ensure all dimensional holistic growth of the learner
Expert Talk Series	I, II, III, IV, V & VI	6	To provide graduates with up-to-date industry relevant knowledge and technological trends in their respective fields

- The members of BoS have also been informed that as per NEP 2020 guidelines, Multiple Entry Multiple Exit (MEME) option has been introduced in the programme.

Sl. No.	Exit Description	Exit Point	Degree/Certificate offered	Goal
1.	First Exit	After completion of First year.	UG Certificate in EEE	The student should be employable as Technical Assistant (EEE) in any industry/organization.
2.	Second Exit	After completion of Second year.	UG Diploma in EEE	The student should be employable as Technician (EEE) in any industry /organization.
3.	Third Exit	After completion of Third year.	B. Voc in EEE	The student should be employable as Technical Supervisor (EEE) in any industry/organization.
4.	Normal Exit	After completion of Fourth year.	B.Tech in EEE	The student should be employable as an Engineer (EEE) in any relevant industry/organization.

- The members of BoS have been informed that the following degrees can be awarded for fast learners (students need to acquire additional credits by completing additional courses/research)
  - B. Tech with “Minor”
  - B. Tech “Honours”
  - B. Tech “Honours with Research”
- The BoS members have been informed that the curriculum is designed to offer four baskets of Programme Electives, each basket having identified courses corresponding to the programme specializations called verticals. This enables learners to grow in a domain specialization or domain vertical. The student can opt for courses in the sequel (PEC-1 to PEC-4) in any of the specific verticals or across the verticals.
 

If a student completes the sequel of courses PEC-1 to PEC-4 listed under a specific vertical, that specific vertical will be mentioned in his/her degree certificate, as specialisation.
- Respected external members have given following inputs and suggestions for the curriculum revision under URR24

***Suggestions Received from External BoS Members***

1.	Dr. M. Shailaja Kumari <i>Professor of EEE, NIT, Warangal</i>	(i) Ensure more number of laboratories in the scheme such that the graduates get more practical knowledge.
2.	Dr. S. Srinivas <i>Professor of EEE, IIT, Madras</i>	(i) UHV-II can be shifted to III/IV semesters (ii) Credits can be given to Environmental Studies and Sports & Yoga courses (iii) Credits for Internship Evaluation can be increased (iv) Rules & Regulations for Multiple Entry and Multiple Exits are to be designed carefully (v) Laboratory experiments should be based only on the course contents discussed in theory part.
3.	Prof. Francesco Grimaccia <i>Dept. of Energy Politecnico di Milano, Italy</i>	(i) Features of measurements (sensitivity, accuracy etc.) can be introduced at the start of the course of Electrical Measurements (ii) Introduction to electricity energy sources can be introduced
4.	Dr. T. Jaganmohan Raj <i>Associate Professor of EEE, UCE, Kothagudem</i>	(i) New laboratories on electric vehicles, advance power electronics can be introduced to reduce gap between academia and industry (ii) Multiple Entry and Multiple Exit options are to be designed such that the students should get benefitted

5.	Sri T. Srimannarayana Murthy <i>Chief Engineer, TSGENCO</i>	(i) Students must do more projects and go for field trips to get practical knowledge
6.	Sri E. Ram Mohan Rao <i>Associate Consultant, TCS, Hyderabad</i>	(i) Soft skills are to be introduced in every semester to improve communication skills.
7.	Dr. Vashist Bist <i>Systems Engineer, Allegro Micro Systems, Pune</i>	(i) Modern technologies are to be introduced. Digital measurements and the latest sensors can be introduced in the Electrical measurements course. (ii) Basics are to be revised in every class of the course content delivery
8.	Dr. Sandeep Madishetti <i>Principal R&amp;D Engineer, Lite-On Singapore Pvt. Ltd., Singapore</i>	(ii) Soft skills and finance related skills are to be enriched among the graduates. Include them in Expert Lecture Series (iii) Hackathon participations, inter disciplinary courses, field trips are to be encouraged for the students
9.	Sri Ch. Ramesh <i>Manager Designs, CG Railway Business Division, Bhopal, Madhya Pradesh</i>	(ii) Microprocessors & Microcontrollers course is to be made as regular course rather than as an elective course
10.	Sri G. Balajose <i>Senior Manager, Software Engineering Sales force, Hyderabad</i>	(i) AI & ML applications are to be introduced in detail so that the student gets expertise in applying them to modern day problems

## RESOLUTIONS

After deliberations, the BoS in Electrical and Electronics Engineering has resolved the following

1. **BoS-URR24 - EEE - August 2024 - R1:**

Resolved to approve the scheme of instruction of B.Tech. EEE programme under URR24

2. **BoS-URR24 - EEE - August 2024 - R2:**

Resolved to approve the syllabus of B.Tech. EEE programme for semesters I & II under URR24

3. **BoS-URR24 - EEE - August 2024 - R3:**

Resolved to approve the rules and regulations of B.Tech. EEE programme under URR24

At the end, the Chairperson, BoS, thanked all the BoS members for giving their suggestions and approving the B.Tech EEE scheme and syllabus under URR24.

The meeting was adjourned at 2.30pm.

**Dr. G. Rajender**  
**Chairperson, BoS of EEE, KITSW**  
**Professor & Head, EEED**

**Members Present :**

S.No.	Name	Position in BoS	
1.	Dr. G. Rajendar <i>Associate Professor &amp; HoD of EEE, KITSW</i>	--- Chairperson, BoS	
2.	Dr. M. Shailaja Kumari <i>Professor of EEE, NIT, Warangal</i>	--- External Member	
3.	Dr. S. Srinivas <i>Professor of EEE, IIT, Madras</i>	--- External Member	
4.	Prof. Francesco Grimaccia <i>Dept. of Energy Politecnico di Milano, Italy</i>	--- External Member	Attended online
5.	Dr. T. Jaganmohan Raj <i>Associate Professor of EEE, UCE, Kothagudem</i>	--- External Member	Attended online

6.	Sri T. Srimannarayana Murthy <i>Chief Engineer, TSGENCO</i>	---	External Member	Attended online
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11	Sri G. Balajose <i>Senior Manager, Software Engineering Sales force, Hyderabad</i>	---	External Member	Attended online
12	All faculty of the department	---	Members	Attended

**Members Present :**

**(Dr. M. Shailaja Kumari)**  
*Professor, NIT, Warangal*

**(Dr. S. Srinivas)**  
*Professor, IIT, Madras*

**Dr. G. Rajendar**  
HoD, EEE

**Prof. V. Ramaiah**  
Professor of EEE

**Dr. C. Venkatesh**  
Professor of EEE

**Dr. V. Rajagopal**  
Professor of EEE

**Sri M. Narasimha Rao**  
Assoc. Prof., EEE

**Dr. B. Jagadish  
Kumar**  
Assoc. Prof., EEE

**Dr. P. Nagarjuna  
Reddy**  
Asst. Prof., EEE

**Dr. G. Sudheer Kumar**  
Assoc. Prof., EEE

**Dr. B. Vijay Kumar**  
Assoc. Prof., EEE

**Sri C. Pavan Kumar**  
Asst. Prof., EEE

**Sri K. Ajith**  
Asst. Prof., EEE

**Sri T. Praveen Kumar**  
Asst. Prof., EEE

**Dr. Y. Manjusree**  
Asst. Prof., EEE

**Dr.D.Rakesh  
Chandra**  
Asst. Prof., EEE

**Dr. A. Rajasekhar**  
Asst. Prof., EEE

**Dr. M. Santhosh**  
Asst. Prof., EEE