KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL-15 (An Autonomous Institute under Kakatiya University)

BOARD OF STUDIES IN ELECTRICAL & ELECTRONICS ENGINEERING

Venue: Digital Simulation Laboratory, Dept. of EEE, Time: 10.30 A.M, Date: 20.06.2015

Agenda:

Finalization of scheme and syllabi of III to VIII semesters (Autonomous) of B.Tech. (Electrical & Electronics Engineering) for the batches admitted in the academic year 2014-15.

Minutes of the meeting:

The BOS unanimously resolved the following

- To go for 6+2 Scheme [6 Theory+ 2 Practical] in the III semester and also to have a mandatory course "Compliance with Current English". In this semester there are 7 Core Courses and 1 Basic Science.
- To go for 5+3 Scheme [5 Theory+ 3 Practical] in the IV semester and to have a mandatory course "Soft and Interpersonal Skills". and also to have mandatory course "Environmental Studies for lateral entry students only". In this semester there are 7 Core Courses and 1 Basic Science.
- To go for 5+3 Scheme [5 Theory+ 3 Practical] in the V semester and also to have a seminar presentation confined to the field of Electrical & Electronics Engineering. In this semester all the courses are core course and also to give weightage to the "Seminar" by allotting 1 Credit.
- To go for 6+2 Scheme [6 Theory+ 2 Practical] in the VI semester and with a mini
 project confined to the field of Electrical & Electronics Engineering. In this
 semester there are 6 Core Courses, 1 Open Elective Course, 1 Professional
 Elective Course and 1 Mini Project with 2 Credits.
- In the Open Elective the students are to be given a option to select subjects concerned with the society viz Disaster Management, Professional Ethics in Engineering and Rural Technology and Community Development.
- To go for 5+2 Scheme [5 Theory+ 2 Practical] in the VII semester and have Major Project work *Phase-I* confined to the field of Electrical & Electronics Engineering. In this semester there are to be 4 Core Courses, 2 Professional Elective Courses and 1 Interdisciplinary course and 1 Major Project Work *Phase-I* with 4 Credits.
- To go for 4+2 Scheme [4 Theory+ 2 Practical] in the VIII semester and also to have Major Project Work *Phase-II* confined to the field of Electrical & Electronics Engineering. In this semester there are to be 3 Core Courses, 2 Professional Elective Courses and 1 Open Elective Course and 1 Major Project Work *Phase-II* with 7 Credits.

III Semester

- EE 212 Network Theory is made into Circuit Theory-I & II and syllabus is modified accordingly.
- EE 213 Basic Electrical Engineering is shifted to I semester in place of which Data Structures is introduced.
- EE 215 Basic Electrical Engineering Laboratory for EEE students is removed and Data Structures Laboratory is included.
- EE 214 Electrical Engineering Materials is removed.
- EI 215 Electronic Devices & Circuits-I in 2nd Year I Semester is shifted to II
 Semester as Basic Electronics Engineering.
- EI 224 Electronic Devices & Circuits-II in 2nd Years II Semester is shifted to III
 Semester as Electronics Circuits.
- EE 223 Electrical Measurements is shifted from 2nd Year II Semester to III Semester as Electrical & Electronic Measurements.
- EE 224 Electromagnetic Fields is shifted from 2nd Year II Semester to III Semester as Electric and Magnetic Fields.
- MH 211 Mathematics-II is renamed as Engineering Mathematics-III.
- EI 2210 Electronic Circuits Laboratory in 2nd Year II Semester is shifted to III Semester as Electric Devices and Circuits Laboratory.

IV Semester

- MH 221 Mathematics-III is renamed as Engineering Mathematics-IV.
- EC 225 Signals and Systems is removed in place of which Circuit Theory-II is included.
- EC314 Linear Integrated Circuits is shifted from 3rd Year I Semester to IV Semester.
- EC 318 Digital Electronics is shifted from 3rd Year I Semester to IV Semester.
- EE 225 Electrical Measurements Laboratory is renamed as Electrical & Electronic Measurements Laboratory.
- EE 217 Networks Laboratory in 2nd Year I Semester is shifted to IV Semester as Circuits and Simulation Laboratory.
- EC 317 Integrated Circuits Laboratory in 3rd Year I Semester is shifted to IV Semester.

V Semester

 HS 311 Management, Economics & Accountancy is shifted from 3rd Year I Semester to VII Semester.

- EI323 Microprocessor & Microcontroller in 3rd Year II Semester is shifted to V Semester. The syllabus to be modified keeping 8085 in view with brief introduction about 8086 upto 3rd Unit and keep 4th Unit same.
- EE 329 Control Systems Laboratory in 3rd Year II Semester is shifted to V Semester.
- EI328 Microprocessor & Microcontroller Laboratory in 3rd Year II Semester is shifted to V Semesters and the experiments to be modified according to the theory.

VI Semester

- EC 326 Digital Signal Processing syllabus to be modified.
- EE 413 Power Semi Conductor Drives in 4th Year I Semester is shifted to VI Semester.
- EE 412 Utilization of Electrical Energy is 4th Year I Semester is shifted to VI Semester.
- EE 414 Professional Elective-I is incorporated in VI Semester
- EE 416 Power Electronics and Drives Laboratory in 4th Year I Semester is shifted to VI Semester as Power Electronics and Simulation Laboratory.

VII Semester

- HS 311 Management, Economics & Accountancy is shifted from 3rd Year I Semester to VII Semester.
- EE 415 Switchgear and Protection is renamed as Power System Protection.
- Professional Elective-II & III are incorporated in VII Semester.
- A New Laboratory named Electric Drives is introduced in VII Semester.

VIII Semester

- Open Elective-II is introduced in VIII Semester.
- Professional Elective-IV & V are incorporated in VIII Semester.
- A New course Renewable Energy Systems is introduced along with the Laboratory.

Meeting concluded at 5 PM on 20.06.2015 and the Chairman Board of Studies thanked all the members.

III Semester

| Course Code | Old Scheme | Course | NI | |
|--|--------------------------------------|----------------------------|--|--|
| MH 211 | Mathematics-II | Code | New Scheme | |
| EE 212 | Network Theory | U14MH 301 | Engineering Mathematics-III | |
| EE 213 | Basic Electrical Engineering | U14EE 302 | Circuit Theory-I | |
| EE 214 | Flectrical Engineering | U14IT 310 | Data Structures | |
| EI 215 | Electrical Engineering Materials | U14EC 304 | Electronic Circuits | |
| | Electronic Devices and circuits-I | U14EE 305 | Electrical and Electronic Measurements | |
| - | Fluid Mechanics & Hydraulic Machines | U14EE 306 | Electric and Magnetic Fields | |
| EE 215 Basic Electrical Engineering Lab EE 217 Networks Laboratory | U14IT 311 | Data Structures Laboratory | | |
| | TVELWOLKS LABORATORY | U14EC 308 | Electronic Devices and Circuits Laboratory | |
| | | U14MH 309 | Compliance with Current English | |

IV Semester

| Course Code | . Old Scheme | Course | |
|----------------|-------------------------------------|-----------|---|
| MH 221 | Mathematics -III | Code ' | New Scheme |
| EE 222 | Electrical Machines-I | U14MH 401 | Engineering Mathematics-IV |
| EE 223 | Electrical Measurements | U14EE 402 | Electrical Machines-I |
| EE 224 | Electromagnetic fields | U14EE 403 | Circuit Theory-II |
| EI 224 | Electronic Doving LG | U14EI 405 | Linear Integrated Circuits |
| EC 225 | Electronic Devices and Circuits -II | U14EI 410 | Digital Electronics |
| EE 225 | Signals & Systems | U14EE 407 | Electrical and Electronic Measurements Laborator |
| I 2210 | Electrical Measurements Lab | U14EE 411 | Circuits and Simulation Laboratory |
| 1 2210 | Electronic Circuits Laboratory | U14EI 412 | Integrated Circuits Laboratory |
| | | U14MH 409 | Soft and Interpersonal Skills |
| • | | U14CH 209 | Environmental Studies (for lateral entry students only) |

V Semester

| Course Code | Old Scheme | Course | |
|----------------|--|-----------|---|
| HS 311 | Management Economics and Accountancy | Code | New Scheme |
| EE 313 | Electrical Machines -II | U14EE 501 | Electrical Machines-II |
| EE 314 | Power systems-I | U14EE 502 | Power Systems-I |
| EE 319 | Control systems F | U14EE 503 | Power Electronics |
| EC 314 | Control systems Engineering Linear Integrated Circuits | U14EE 506 | Control Systems Engineering |
| EC 318 | Digital Electronics | U14EC 510 | Microprocessors and Microcontrollers |
| EE 316 | Electronics | U14EE 507 | Electrical Machines-I Laboratory |
| EC 317 | Electrical Machines Lab-I | U14EE 508 | Control Systems Laboratory |
| 00317 | Integrated Circuits Lab | U14EC 511 | Microprocessors & Microcontrollers Laboratory |
| | | U14EE 509 | Seminar |

VI Semester

| Course | Old Scheme | Course | No. C.1 |
|--------|---|------------|--|
| OE 321 | Open Elective | Code | New Scheme |
| EE 322 | Power Electronics | U14 OE 601 | Open Elective-I |
| EE 324 | Power Systems-II | U14EE 602 | Power Systems-II |
| EC 326 | Digital Signal Processing | U14EI 603 | Digital Signal Processing |
| EI 323 | Migra Dua Migra Dua | U14EE 604 | Power Semiconductor Drives |
| EE 327 | Micro Processor and Micro Controllers | U14EE 605 | Utilization of Electrical Energy |
| | Electrical Machines Lab-II | U14EE 606 | Professional Elective-I |
| EI 328 | Micro Processor and Micro Controllers Lab | U14EE 607 | |
| EE 329 | Control Systems Lab | | Power Electronics and Simulation Laboratory |
| | Control Systems Lab | U14EE 608 | Electrical Machines-II Laboratory |
| | | U14EE 609 | Mini Project Work |

Old

Open Electives:
OE 321 A. Operations Research
OE 321 B. Management Information Systems
OE 321 C. Entrepreneurship Development
OE 321 D. Forey & Foreign Trade

Members Present

| 1 | Prof. P. Venugopal Rao Head, Dept. of EEED, KITS, Warangal-506 015 | | Chairperson, BoS of EEE R-Control |
|----|--|----------|------------------------------------|
| 2 | . Sri M. Narasimha Rao Assoc. Prof. of EEED, KITSW | | Member 720/6/15 |
| 3. | Sri G. Rajendar Assoc. Prof. of EEED, KITSW | | Member |
| 4. | Sri B. Jagadish Kumar Assoc. Prof. of EEED, KITSW | <u> </u> | Member By |
| 5. | Sri P. Nagarjuna Reddy Asst. Prof. of EEED, KITSW | ; | Member p. N or |
| 6. | Dr. M. Sydulu Prof. of EED, NIT, Warangal | | External Member Mydelus |
| 7. | Dr.V.T.Somasekhar, Prof. of EED, NIT, Warangal | | External Member V) omasculo |
| | Prof. V. Ramaiah Prof. of EEED, KITSW | | Co-Opted Member ORamail 20/06/2015 |
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