



KARATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
Opp. Yerragada Guda, Huzaripally (Mandal), WARANGAL - 506 015, Telangana, INDIA.
వరంగల్ జిల్లాలోని యెర్రగూడ గూడ, హుజూరిపల్లి (మండలం), వరంగల్ - 506 015 తెలంగాణ, భారతదేశం.
KITSW
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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

No.231/KITSW/EEE/2021-22/DAAC_MoM_VI&VII Sem Scheme Revision Date: 14.05.2022

Submitted to the Principal:

Sub: Inclusion of Advanced Data Structures (Theory & Laboratory) in VI Sem and Revision of Scheme for VI & VII Semesters - Reg.

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With the subject cited above, the DAAC committee has resolved to include "Advanced Data Structures" (Theory & Laboratory) in VI Semester and also resolved to Revise the Scheme for VI & VII Semesters vide DAAC meeting conducted on 13/05/2022.

Minutes of the meeting as follows:

1. It is resolved to include "Advanced Data Structures" Theory course with 3 credits in VI semester.
2. It is resolved to include "Advanced Data Structures Laboratory" course with 1 credit in VI semester.
3. It is resolved to shift the course "Management, Economics and Accountancy" to VII semester.
4. It is resolved to reduce credits for the course "Control System Engineering" from 4 to 3.
5. It is resolved to revise the scheme of B.Tech EEE VI and VII semesters.
6. It is resolved to shift "Signals and Systems" from PCC to Professional Elective-II Section in VI Semester.

The copies of MoM and Proposed Scheme of Instructions & Evaluation for VI & VII Semesters B.Tech. (EEE) are herewith enclosed, for your kind perusal.

C. Venkatesh
(Dr. C. Venkatesh)
Prof. & Head
Dept. of EEE

MINUTES OF THE MEETING OF DEPARTMENT ACADEMIC ADVISORY COMMITTEE (DAAC) HELD AT 2.00PM ON 13.05.2022, IN B-III 107 OF THE DEPARTMENT.

Agenda:

1. Inclusion of Advanced data structures & Advanced data structures lab in VI semester courses.
2. Revision of Scheme for VI & VII semester
3. Any other Academic Matters

Date: 13.05.2022

Time: 02.00pm

The following members were present:-

1.	Prof. C. Venkatesh Professor & Head, EEED, KITSW	Chairman
2.	Prof. V. Ramaiah Professor, EEED, KITSW	Member
3.	Prof. V. Rajagopal Professor, EEED, KITSW	Member
4.	Sri M. Narasimha Rao Associate Professor, EEED, KITSW	Member
5.	Dr. G. Rajender Associate Professor, EEED, KITSW	Member
6.	Dr. B. Jagadish Kumar Associate Professor, EEED, KITSW	Member
7.	Dr. G. Rajender Naik Associate Professor, EEED, KITSW	Member
8.	Dr. G. Sudheer Kumar Associate Professor, EEED, KITSW	Member
9.	Dr. B. Vijay Kumar Associate Professor, EEED, KITSW	Convener

The meeting commenced at 02.00 PM and was presided over by the Chairman, DAAC. At the outset, the Chairman, DAAC welcomed the members to the meeting to discuss the pre-notified items on the agenda and approval.

Minutes of the meeting:

1. It is resolved to include "Advanced data structures" theory course with 3 credits in VI semester.
2. It is resolved to include "Advanced data structures laboratory" course with 1 credit in VI semester.
3. It is resolved to shift the course "Management, Economics and Accountancy" to VII semester.
4. It is resolved to reduce credits for the course "Control System Engineering" from 4 to 3.
5. It is resolved to revise the scheme of B.Tech EEE VI and VII semesters.

Handwritten signatures and initials:
By WS Rajagopal
Ch 172

6. It is resolved to shift "Signals and systems" from PCC to OE course.

The meeting adjourned at 3:00 PM after thanking all the members for participation and useful discussions.

1. Prof. C. Venkatesh
Professor & Head, EEED, KITSW
2. Prof. V. Ramaiah
Professor, EEED, KITSW
3. Prof. V. Rajagopal
Professor, EEED, KITSW
4. Sri M. Narasimha Rao
Associate Professor, EEED, KITSW
5. Dr. G. Rajender
Associate Professor, EEED, KITSW
6. Dr. B. Jagadish Kumar
Associate Professor, EEED, KITSW
7. Dr. G. Rajender Naik
Associate Professor, EEED, KITSW
8. Dr. G. Sudheer Kumar
Associate Professor, EEED, KITSW
9. Dr. B. Vijay Kumar
Associate Professor, EEED, KITSW

Chairman

Member

Member

Member

Member

Member

Member

Member

Convener

C. Venkatesh
(Prof. C. Venkatesh)
Professor & Head
Dept. of EEE



Proposed Revision

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
 KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15
 (An Autonomous Institute under Kakatiya University, Warangal)
 PROPOSED SCHEME OF INSTRUCTIONS & EVALUATION
 VI-SEMESTER OF 4-YEAR B.TECH DEGREE PROGRAMME

(6Th+3P+1Mini Project)

B. Tech in Electrical & Electronics Engineering
 GAC meeting held on
 13/5/22
 S. R. S. 13/5/22

Sl.No	Course Category	Course Code	Course Name	Periods/week				Credits	Evaluation Scheme				
				L	T	P	C		TA	MSE	Total	ESE	Total Marks
1	HSNC	U18TP601	Quantitative Aptitude & Logical Reasoning	2	-	-	1	10	30	40	60	100	
2	PE	U18EE603	Professional Elective - II / MOOC-II	3	-	-	3	10	30	40	60	100	
3	PCC	U18EE604	Power System Operation and Control	3	-	-	3	10	30	40	60	100	
4	PCC	U18EE605	Power Semiconductor Drives	3	-	-	3	10	30	40	60	100	
5	PCC	U18EE606	Control Systems Engineering	3	-	-	3	10	30	40	60	100	
6			Advanced Data Structures	3	-	-	3	10	30	40	60	100	
7	PCC	U18EE607	Control Systems Engineering Laboratory	-	-	2	1	40	-	40	60	100	
8	PCC	U18EE608	Power Semiconductor Drives Laboratory	-	-	2	1	40	-	40	60	100	
9			Advanced Data Structures Laboratory	-	-	2	1	40	-	40	60	100	
10	PROJ	U18EE610	Mini Project	-	-	2	1	100	-	100	-	100	
Additional Learning*				Maximum credits allowed for Honours/Minor				-	-	-	-	-	-
				Total credits for Honours/Minor students:				20+7	-	-	-	-	-
				Total	17	-	6	20	280	180	460	540	1000

* List of courses for additional learning through MOOCs towards Honours/Minor in Engineering shall be prescribed by the department under Honours/Minor Curricula

Note: L - Lectures; T - Tutorials; P - Practicals; CIE - Continuous Internal Evaluation; TA - Teachers Assessment; MSE - Mid Semester Examination; ESE - End Semester Examination;

Student Contact Hours/Week : 23
 Total Credits(C) : 20
 Professional Elective-II / MOOC - II
 U18EE 603A: Utilization of Electrical Energy
 U18EE 603B: High Voltage Engineering
 U18EE 603C: Electric Vehicles
 U18EE 603D: Signals & Systems
 U18EE 603M: MOOCs Course

ref: Dr. Ravi K. P. R. of

Curriculum Revision: UPR-18 (R) - 2022

- Advanced Data Structures Course (theory) introduced in B.Tech EEE - 6th Sem.
- Advanced Data Structures Lab. Introduced in B.Tech EEE - 6th Sem.
- Control Systems Engineering (theory) coverage is reduced from 4 to 3, in B.Tech EEE - 6th Sem.
- Signals & Systems (theory) coverage is reduced from 4 to 3, in B.Tech EEE - 6th Sem.

C. Venkatesh 13/5/22
 HOD, EEE D1



DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
 KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15
 (An Autonomous Institute under Kakatiya University, Warangal)
 PROPOSED SCHEME OF INSTRUCTIONS & EVALUATION
 VII-SEMESTER OF 4-YEAR B.TECH DEGREE PROGRAMME

B. Tech in Electrical & Electronics Engineering

(9th+2P+1MC+1Major Project)

Sl.No	Course Category	Course Code	Course Name	Periods/week				Credits	Evaluation Scheme				Total Marks
				L	T	P	C		TA	MSE	Total	ESE	
1	OE	U18OE701	Open Elective-III	3	-	-	3	10	30	40	60	100	
2	PE	U18EE702	Professional Elective - III / MOOC-III	3	-	-	3	10	30	40	60	100	
3	PE	U18EE703	Professional Elective - IV / MOOC-IV	3	-	-	3	10	30	40	60	100	
4	PCC	U18EE704	Power System Protection	3	-	-	3	10	30	40	60	100	
5	HSMC		Management, Economics and Accountancy	3	-	-	3	10	30	40	60	100	
6	PCC	U18EE705	Electrical Simulation Laboratory	-	-	2	1	40	-	40	60	100	
7	PCC	U18EE706	Power Systems Laboratory	-	-	2	1	40	-	40	60	100	
8	PROJ	U18EE707	Major Project - Phase - I	-	-	6	3	100	-	100	-	100	
9	MC	U18EE708	Internship Evaluation	-	-	2	-	100	-	100	-	100	
Additional Learning*:				Maximum credits allowed for Honours/Minor				20					
				Total credits for Honours/Minor students:				20+7					
				Total credits for MOOCs towards Honours/Minor in Engineering shall be prescribed by the department under Honours/Minor Curricula				7					
				15	-	-	12	330	150	480	420	900	

* List of courses for additional learning through MOOCs towards Honours/Minor in Engineering shall be prescribed by the department under Honours/Minor Curricula

Note: L - Lectures; T - Tutorials; P - Practicals; CIE - Continuous Internal Evaluation; TA - Teachers Assessment; MSE - Mid Semester Examination; ESE - End Semester Examination;

Student Contact Hours/Week : 27
 Total Credits(C) : 20

Open Elective-III
 OE 701A: Disaster Management
 OE 701B: Project Management
 OE 701C: Professional Ethics in Engineering
 OE 701D: Rural Technology and Community Developments

Professional Elective-III/MOOC-III
 U18EE702A: HVDC & FACTS
 U18EE702B: Embedded Systems
 U18EE702C: Micro grid & Distributed Generation
 U18EE702M: MOOCs Course

Professional Elective-IV/MOOC-IV
 U18EE703A: Computer Methods in Power Systems
 U18EE703B: Power Quality
 U18EE703C: Power System Deregulation
 U18EE703M: MOOCs Course

Case-studies
 Note: management, Economics and Accountancy course is shifted from 6th sem to 7th sem B.Tech EE (VPR-18)

Handwritten notes and signatures at the bottom of the page, including a signature and the text "c. N. Venkatesh 13/5/22" and "100 D, CEED".