

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

No.231/KITSW/EEE/2021-22/DAAC_MoM_VI&VII Sem Scheme Revison

Date: 14.05.2022

Submitted to the Principal:

Sub: Inclusion of Advanced Data Structures (Theory & Laboratory) in VI Sem and Revision of Scheme for VI & VII Semesters - Reg.

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With the subject cited above, the DAAC committee has resolved to include "Advanced Data Structures" (Theory & Laboratory) in VI Semester and also resolved to Revise the Scheme for VI & VII Semesters vide DAAC meeting conducted on 13/05/2022.

Minutes of the meeting as follows:

- 1. It is resolved to include "Advanced Data Structures" Theory course with 3 credits in VI semester.
- It is resolved to include "Advanced Data Structures Laboratory" course with 1 credit in VI semester.
- It is resolved to shift the course "Management, Economics and Accountancy" to VII semester.
- It is resolved to reduce credits for the course "Control System Engineering" from 4 to 3.
- 5. It is resolved to revise the scheme of B.Tech EEE VI and VII semesters.
- It is resolved to shift "Signals and Systems" from PCC to Professional Elective-II Section in VI Semester.

The copies of MoM and Proposed Scheme of Instructions & Evaluation for VI & VII Semesters B.Tech. (EEE) are herewith enclosed, for your kind perusal.

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. (Dr. C. Venkatesh) Prof. & Head Dept. of EEE

MINUTES OF THE MEETING OF DEPARTMENT ACADEMIC ADVISORY COMMITTEE (DAAC) HELD AT 2.00PM ON 13.05.2022, IN B-III 107 OF THE DEPARTMENT.

Agenda:

- Inclusion of Advanced data structures & Advanced data structures lab
 in VI semester courses.
- 2. Revision of Scheme for VI & VII semester
- 3. Any other Academic Matters

Date: 13.05.2022

Time: 02.00pm

The following members were present:-

1.	Prof. C. Venkatesh Professor & Head, EEED, KITSW	Chairman
2.	Prof. V. Ramaiah Professor, EEED, KITSW	Member
3.	Prof. V. Rajagopal Professor, EEED, KITSW	Member
4.	Sri M. Narasimha Rao Associate Professor, EEED, KITSW	Member
5.	Dr. G. Rajender Associate Professor, EEED, KITSW	Member
6.	Dr. B. Jagadish Kumar Associate Professor, EEED, KITSW	Member
7.	Dr. G. Rajender Naik Associate Professor, EEED, KITSW	Member
8.	Dr. G. Sudheer Kumar Associate Professor, EEED, KITSW	Member
9.	Dr. B. Vijay Kumar Associate Professor, EEED, KITSW	Convener

The meeting commenced at 02.00 PM and was presided over by the Chairman, DAAC. At the outset, the Chairman, DAAC welcomed the members to the meeting to discuss the pre-notified items on the agenda and approval.

Minutes of the meeting:

- It is resolved to include "Advanced data structures" theory course with 3 credits in VI semester.
- 2. It is resolved to include "Advanced data structures laboratory" course with 1 credit in VI semester.
- It is resolved to shift the course "Management, Economics and Accountancy" to VII semester.
- 4. It is resolved to reduce credits for the course "Control System Engineering" from 4 to 3.
- 5. It is resolved to revise the scheme of B.Tech EEE VI and VII semesters.

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6. It is resolved to shift "Signals and systems" from PCC to OE course.

The meeting adjourned at 3:00 PM after thanking all the members for participation and useful discussions.

 Prof. C. Venkatesh Professor & Head, EEED, KITSW

 Prof. V. Ramaiah Professor, EEED, KITSW

3. Prof. V. Rajagopal Professor, EEED, KITSW

 Sri M. Narasimha Rao Associate Professor, EEED, KITSW

Dr. G. Rajender
 Associate Professor, EEED, KITSW

 Dr. B. Jagadish Kumar Associate Professor, EEED, KITSW

 Dr. G. Rajender Naik Associate Professor, EEED, KITSW

8. Dr. G. Sudheer Kumar Associate Professor, EEED, KITSW

 Dr. B. Vijay Kumar Associate Professor, EEED, KITSW Chairman

Member

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Member

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Member -

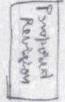
Member

Convener -

C. Jenkatosh

(Prof. C. Venkatesh) Professor & Head Dept. of EEE





KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE: WARANGAL 15 DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

VI-SEMESTER OF 4-YEAR B.TECH DEGREE PROGRAMME PROPOSED SCHEME OF INSTRUCTIONS & EVALUATION (An Autonomous Institute under Kakatiya University, Warangal)

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VII-SEMESTER OF 4-YEAR B.TECH DEGREE PROGRAMME PROPOSED SCHEME OF INSTRUCTIONS & EVALUATION (An Autonomous Institute under Kakatiya University, Warungal)

(Gh+2P+1MC+1Major Project)

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Note: L - Lectures; T - Tutorials; P - Practicals; CIE - Continuous Internal Evaluation; TA - Teachers Assessment; MSE - Mid Semester Examination;

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20 U18EE702B: Embedded Systems U18EE702A: HVDC & FACTS Professional Elective-III/MOOC-III

U18EE703A: Computer Methods in Power Systems

Professional Elective-IV / MOOC-IV

Open Elective-III

Total Credits(C)

OE 701A: Disaster Management -OE 701B: Project Management

OE701C: Professional Ethics in Engineering

U18EE702C: Micro grid & Distributed Generation U18EE703B: Power Quality

U18BE703C: Power System Deregulation U18EE703M: MOOCs Course

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