

ISO 9001:2015 AICTE-CI: GOLD Category Institute NAAC-'A' Grade Institute (CGPA: 3.21) NIRF-2021 Rank: 197

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
 Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.
 కాకతీయ టెక్నోలజీ మరియు విజ్ఞాన సంస్థానం, వరంగల్ - 506 015 తెలంగాణ, భారత
 కర్ణాటక సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, కర్ణాటక - 506 015 తెలంగాణ, భారతదేశం

KITSW
 (An Autonomous Institute under Kakatiya University, Warangal)
 (Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by KRASHIA EDUCATION SOCIETY)
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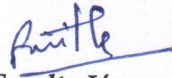
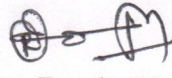
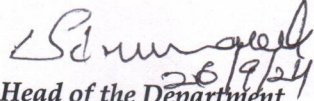
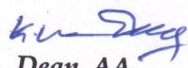
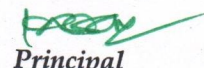
ACADEMIC YEAR 2024-25 : ODD SEMESTER
Scheme: PRR20
M.Tech. Embedded System & VLSI

2024-25
 ES&VLSI
 II-FLOOR
 B I 220
 w.e.f.
 17.10.2024

Class Teacher: Sri B. Venumaheshwar

Day	09.10-10.00	10.00-10.50	10.50 - 11.40	11:40-12:30	L U N C H B R E A K	13.30-14.20	14.20-15.10	15:10-16:00
MON	MBES	DICD	DICD(T)	ESD		RMIPR SH-3-1	MBES (T) B-I-217	EPRW SH-3-1
TUE	EPRW SH-3-1	MBES	SVDV	SVDV(T)		→ MBES LAB (B-I-201) ← (KSC)		
WED	→ DICD LAB (B-I-219) ← (RND)			-		-	-	-
THU	RMIPR SH-3-1	ESD	SVDV	DICD		-	-	-
FRI	SVDV	MBES	ESD	-		DICD	ESD(T)	-
SAT	-	-	-	-		-	-	-

Course Title	Course Code & Course Name	Course Faculty
DICD	P20EV101: Digital IC Design	- Smt. R. Nirmala Devi (RND)
MBES	P20EV102: Microcontroller based Embedded Systems	- Dr. K. Srinivas(KSC)
SVDV	P20EV103B: System Verilog for Design and Verification	- Dr. O. Anjaneyulu
ESD	P20EV104A: Embedded System Design	- Sri B. Venumaheshwar (BVM)
RMIPR	P20MC107: Research Methodology and IPR	- Dr. J. Laxman
ERPW	P20AC108A: English for Research Paper Writing	- Smt. B. Sravanthi (BS)
DICD LAB	P20EV105: Digital IC Design Laboratory	- Smt. R. Nirmala Devi (RND)
MCES LAB	P20EV106 : Microcontroller based Embedded Systems Laboratory	- Dr. K. Srinivas(KSC)/.

 Faculty I/c, Dept. Timetables
 Faculty I/c, Overall Timetables
 Head of the Department
 Dean, AA
 Principal