

**ACADEMIC YEAR 2024-25: EVEN SEMESTER**  
**SECOND SEMESTER TIMETABLE**  
**M.Tech. ES&VLSI**

Class Teacher: Sri G. Raju

DAY/TIME	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.40 - 12.30	L U N C H B R E A K	13.30 - 14.20	14.20 - 15.10	15.10 - 16.00
MONDAY	ASCD	AICD	DM SH-3-1	ESDWRTOS		→AICD Lab- (B-I-219)← RND		
TUESDAY	DM SH-3-1	LVLSI	AICD	ASCD		ESDWRTOS	LVLSI (T)	ASCD (T)
WEDNESDAY	LVLSI	ESDWRTOS	ASCD	AICD		ESDWRTOS (T)	Library	Library
THURSDAY	→Mini Project←			LVLSI		AICD (T)	Library	Library
FRIDAY	→Mini Project←					→ASCD Lab (B-I-201)← GR		
SATURDAY	-	-	-	-		-	-	-

Course Title	Course Code & Course Name	Course Faculty
AICD	P20EV201: Analog IC Design	Smt. R. Nirmala Devi (RND)
ASCD	P20EV202: Advanced System On Chip Design	Sri G. Raju (GR)
LVLSI	P20EV203A: Low Power VLSI Design	Dr. K. Srinivas (KSC)
ESDWRTOS	P20EV204A: Embedded Systems Design with RTOS	Sri B. Shashikanth(BSK)
DM	P20AC208D: Disaster Management	Sri K. Srujan Varma
AICD Lab	P20EV205: Analog IC Design Laboratory	Smt. R. Nirmala Devi (RND)
ASCD Lab	P20EV206: Advanced System On Chip Design Laboratory	Sri G. Raju (GR)
Mini Project	P20EV207: Mini Project with Seminar	Dr. B. Jeevan (BJ)

*Sri G. Raju*  
 Faculty I/c,  
 Dept. Timetables

*Sri G. Raju*  
 Faculty I/c,  
 Overall Timetables

*S. Srinivas*  
 Head of the Department

*K. Srinivas*  
 Dean, AA

*Sri G. Raju*  
 Principal