Four-weeks Industry supported Summer Internship Programme

Analog & Digital VLSI Design using Cadence Design Tools: A Hands-On Approach

16th June – 12th July, 2025 Registration Form

Name:	
Roll No:	
Branch: Semester:	
Organization:	
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Correspondence Address:	
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PIN code:	
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Registration Link:	
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Date: Signature of the Stu	ident

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Chairman, KITSW

Patron:

Sri P. Narayana Reddy, Treasurer, KITSW

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Co-Coordinator:

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- Dr.Md.A.Muqueem, Asst.Prof, ECE

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- Sri. A.Pavan ,Asst.Prof,ECE
- Dr.T.Sunil Kumar, Asst.Prof,ECE

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A Four-weeks Industry supported Summer Internship Programme

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16th June - 12th July, 2025



Organized by Department of **Electronics and Communication** Engineering (ECE), KITS, WARANGAL, Telangana

ABOUT KITSW:

Kakatiya Institute of Technology & Science, Warangal (KITSW) was established in the year 1980 under Ekasila Education Society (EES). Over the years it has developed into a premier institute of higher learning and is rated now as one of the AAA graded engineering college and placed among the top 50 private engineering colleges in India. It is a residential campus spread over 70 acres with excellent infrastructure in the form of state of the art library, seminar halls, guest houses and laboratories.

There are 13 departments offering 11 under graduate & 8 post graduate programs besides doctoral programmes. Around 4000 students study on the campus. All the academic departments are accredited by the National Board of Accreditation (NBA), New Delhi.

ABOUT ECE DEPARTMENT:

The Department of Electronics and Communication Engineering was established in the year 2000 with an initial intake of 45 students, which has progressively increased to 180, reflecting its growing academic strength. The department achieved its first accreditation from the National Board of Accreditation (NBA),

New Delhi, in 2008 under Tier II for a period of three years. Since then, it has maintained its quality standards through timely re accreditations, with the current accreditation status valid until June 2027.

In the academic year 2020-21, the department introduced an M.Tech program in Communication Engineering and Signal Processing (CESP) with an initial intake of 30, which was later revised to 12 from the academic year 2021-22 to enhance the focus on quality education and research.

The department is supported by a team of 32 highly qualified and dedicated faculty members, including 03 Professors, 07 Associate Professors, and 22 Assistant Professors. Currently, 17 faculty members hold Ph.D. degrees, while 15 are actively pursuing their doctoral research, strengthening the department's academic and research credentials.

The Department of Electronics and Communication Engineering at KITSW committed to excellence in education, research, and industry collaboration. The department has received sponsored research funding from prestigious agencies such as DST, SERB, and MSME, fostering innovation in cutting-edge domains. To enhance faculty expertise and student learning, the department has conducted Faculty Development Programs (FDPs) and Short-Term Training Programs (STTPs) sponsored by DST and E&ICT (NIT Warangal).

Strengthening academia-industry interaction, the department has also established MoUs with leading industries, ensuring practical exposure and skill development for students.

ABOUT INTERNSHIP PROGRAMME:

The Four-week Industry supported Summer Internship Programme on CADENCE tool is to gain practical aspects, to provide hands-on experience and skill development, In the field of VLSI Domain, This program allows students to explore different areas of Electronics design, from Chip design to packaging and boards.

This internship will also allows to:

- Build a strong foundation in CADENCE
 Design and development.
- Understand real-world applications of automated testing and data analysis.
- Enhance problem-solving skills through practical projects and tasks.
- Learn from experienced professionals and gain exposure to industry practices.

Participating in this internship aligns you with academic goals and career aspirations in VLSI & Embedded systems, and engineering innovation. It's a valuable opportunity to bridge the gap between theory and application.